# **WEST Search History**

Hide Items Restore Clear Cancel

DATE: Thursday, June 17, 2004

Hide?	<u>Set</u> Name	Query	<u>Hit</u> Count
	DB=F	GPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
	L14	L13 not l11	2
n	L13	graphics near3 (\$5proces\$4 or engine or accelerator or logic or module or device or unit) same (shar\$3 or unif\$4) near3 cache same (cpu or main processing unit or central processing unit or processing unit)	28
	L11	graphics adj3 (\$5proces\$4 or engine or accelerator or logic or module or device or unit) same (shar\$3 or unif\$4) near3 cache same (cpu or main processing unit or central processing unit or processing unit)	26
	L10	L9 not 18	12
D	L9	graphics adj3 (\$5proces\$4 or engine or accelerator or logic or module or device or unit) same graphics adj3 cache same (cpu or main processing unit or central processing unit or processing unit)	28
	L6	L5 not l4	16
	L5	graphics adj3 \$5proces\$4 same graphics adj3 cache same (cpu or main processing unit or central processing unit or processing unit)	18
	L4	graphics \$5proces\$4 same graphics cache same (cpu or main processing unit or central processing unit or processing unit)	2
	L1	graphics \$5proces\$4 same graphics cache same cpu cache	1

END OF SEARCH HISTORY



Hide Items Restore Clear Cancel

DATE: Thursday, June 17, 2004

Hide?	<u>Set</u> <u>Name</u>	Query	<u>Hit</u> Count
	DB=P	GPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
	L10	L9 not 18	12
	L9	graphics adj3 (\$5proces\$4 or engine or accelerator or logic or module or device or unit) same graphics adj3 cache same (cpu or main processing unit or central processing unit or processing unit)	28
	L6	L5 not l4	16
	L5	graphics adj3 \$5proces\$4 same graphics adj3 cache same (cpu or main processing unit or central processing unit or processing unit)	18
	L4	graphics \$5proces\$4 same graphics cache same (cpu or main processing unit or central processing unit or processing unit)	2
	L1	graphics \$5proces\$4 same graphics cache same cpu cache	1

END OF SEARCH HISTORY

## Refine Search

#### Search Results -

Terms	Documents	
L16 not L17	31	

US Pre-Grant Publication Full-Text Database US Patents Full-Text Database

US Patents Full-Text Database
US OCR Full-Text Database

Database:

EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index

IBM Technical Disclosure Bulletins

Search:

L18	Rel

Refine Search

Recall Text 👄	c



Interrupt

### Search History

## DATE: Thursday, June 17, 2004 Printable Copy Create Case

Set Name side by side	Query	<u>Hit</u> Count	Set Name result set
DB=	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
<u>L18</u>	L16 not 117	31	<u>L18</u>
<u>L17</u>	L16 same color	14	<u>L17</u>
<u>L16</u>	L15 same texture	45	<u>L16</u>
<u>L15</u>	graphics near3 (\$5proces\$4 or engine or accelerator or logic or module or device or unit) same cache same (cpu or main processing unit or central processing unit or processing unit)	502	<u>L15</u>
<u>L14</u>	L13 not 111	2	<u>L14</u>
<u>L13</u>	graphics near3 (\$5proces\$4 or engine or accelerator or logic or module or device or unit) same (shar\$3 or unif\$4) near3 cache same (cpu or main processing unit or central processing unit)	28	<u>L13</u>
<u>L12</u>	L11 not 110	22	<u>L12</u>
<u>L11</u>	graphics adj3 (\$5proces\$4 or engine or accelerator or logic or module or device or unit) same (shar\$3 or unif\$4) near3 cache same (cpu or main processing unit or central processing unit or processing unit)	26	<u>L11</u>

<u>L10</u>	L9 not 18	12	<u>L10</u>
<u>L9</u>	graphics adj3 (\$5proces\$4 or engine or accelerator or logic or module or device or unit) same graphics adj3 cache same (cpu or main processing unit or central processing unit or processing unit)	28	<u>L9</u>
<u>L8</u>	L5 not 14	16	<u>L8</u>
<u>L7</u>	L6 and 12	0	<u>L7</u>
<u>L6</u>	L5 not 14	16	<u>L6</u>
<u>L5</u>	graphics adj3 \$5proces\$4 same graphics adj3 cache same (cpu or main processing unit or central processing unit or processing unit)	18	<u>L5</u>
<u>L4</u>	graphics \$5proces\$4 same graphics cache same (cpu or main processing unit or central processing unit or processing unit)	2	<u>L4</u>
<u>L3</u>	(5008838  5559952  5828382  5844576  5870105  5877779  5912673)![pn]	14	<u>L3</u>
<u>L2</u>	(5008838  5559952  5828382  5844576  5870105  5877779  5912673)![pn]	14	<u>L2</u>
<u>L1</u>	graphics \$5proces\$4 same graphics cache same cpu cache	1	<u>L1</u>

## END OF SEARCH HISTORY